

Capacitor and Battery Analyzer

> Specially designed for electric-double layer capacitors (EDLCs) , battery, and solar cell research Type: VK-CA-8000

> Source and measure up to 20V and 10A

Versatile control software provides all necessary data analyzing tools with automated curve fitting to evaluate capacitance, power, energy of EDLCs, and capacity, cycles life testing of batteries.

Constant current charge-discharge test Cyclic voltammetry curve Self discharge analysis etc...

Can be used as a potentiostat or a galvanostat with 4 probes



Specifications	
Measurement Range	Max. Voltage: 20 V Max. Current: 10 A (pulse) 8 A (continuous) with 5½-digits measuring resolution
Measuring Technique	Digital Source/Measure Unit
Inputs	Front: 4 probes
A/D Converters	24 Bit (2 independent ADCs for V & I readings) up to 30,000 SPS
User Interface and data collection	Computer software is provided for control of all functions and data logging. Measurement data can be saved as a text file and directly plotted on *Microsoft Excel graph. (Windows based PC required)
Communication	Through a USB port
Power Requirement	100 – 240 VAC (50-60 Hz)
Dimensions, Weight	320 mm(W) x 450 mm(D) x 150 mm(H) , 10 kg

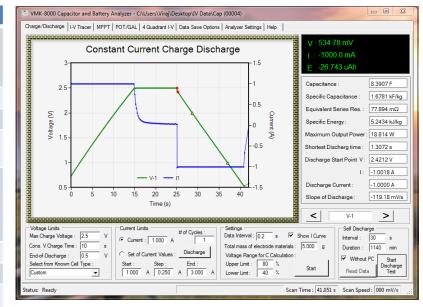
Built-in Software Features

For EDLCs Analysis:

- ✓ Measurement of constant current charge-discharge test with given voltage compliance.
- ✓ Plot cyclic voltammetry curve
- Curve fittings for ideal RC model and nonlinear real model R-CPE(Q, α).
- ✓ Calculation of energy vs. time plots
- ✓ Calculation of energy vs. power curve
- Self discharge analysis
- ✓ Cycle life testing

For Battery Analysis:

- Measurement of constant current charge-discharge curve with given voltage compliance.
- ✓ Analysis of both charge and discharge data
- ✓ Self discharge analysis
- ✓ Cycle life testing
- Limit based analyses (voltage, current, temperature, ohmic value)



Built-in Software Features

General capabilities:

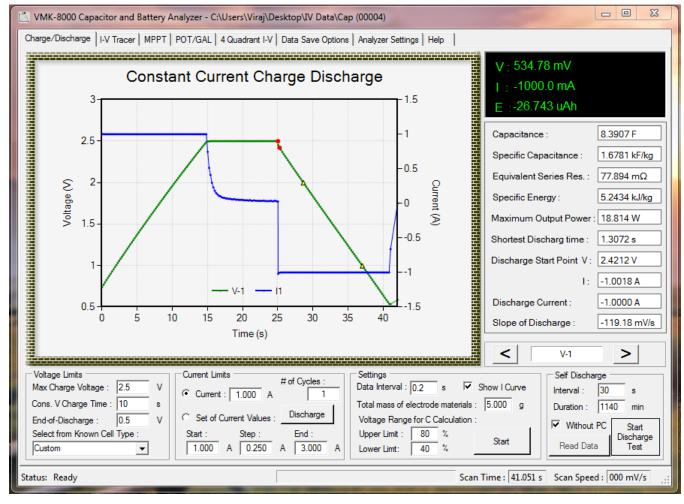
- ✓ I-V tracing for solar cell
- Use as a potentiostat with 4 wires
- ✓ Use as a galvanostat
- √ 4-probe resistance measurement
- ✓ Use as lead-acid, Li-ion, Li polymer battery charger up to 8A and 20 V
- ✓ All data and graphs can be directly saved as ®Microsoft Excel files
- All measurement parameters can be saved and reloaded when needed to repeat at the same experiment.

SPD Laboratory, Inc.

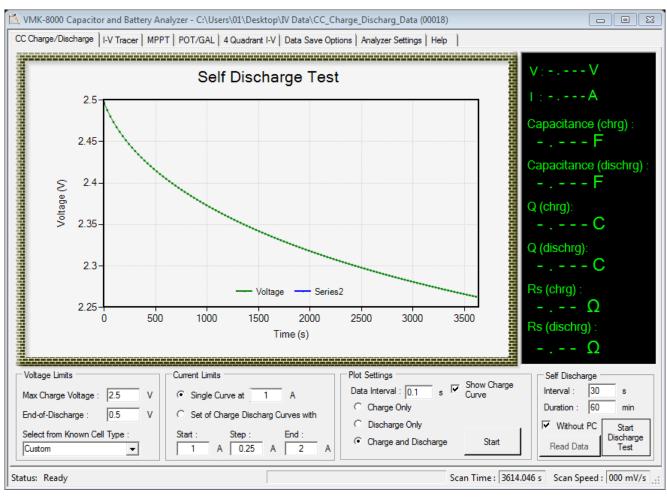
2-35-1 Johoku, Hamamatsu, 432-8011, JAPAN Tel: +81-53-474-7901 Fax: +81-53-401-7080

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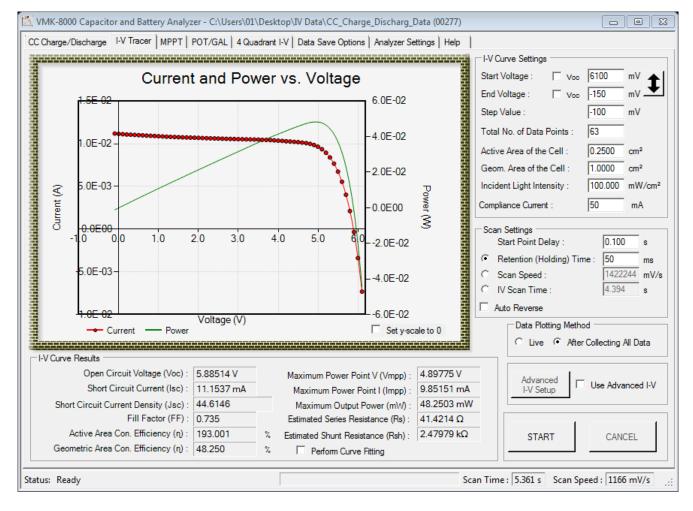
Web: http://www.spdlab.com/English/VK-PA-25.html



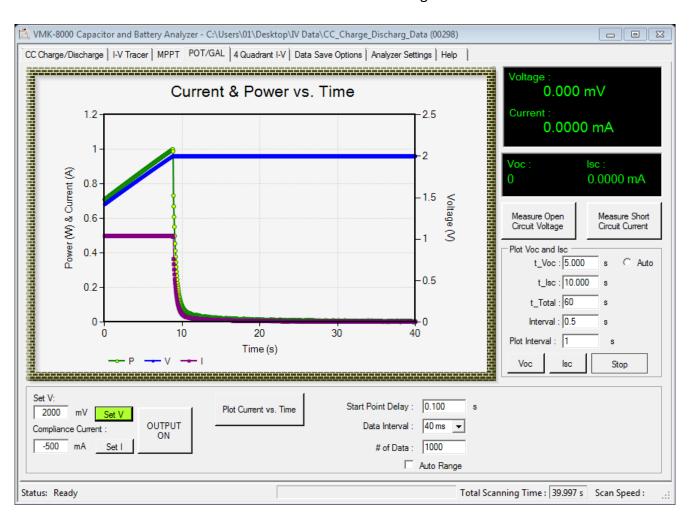
Screenshot of constant current charge discharge control panel



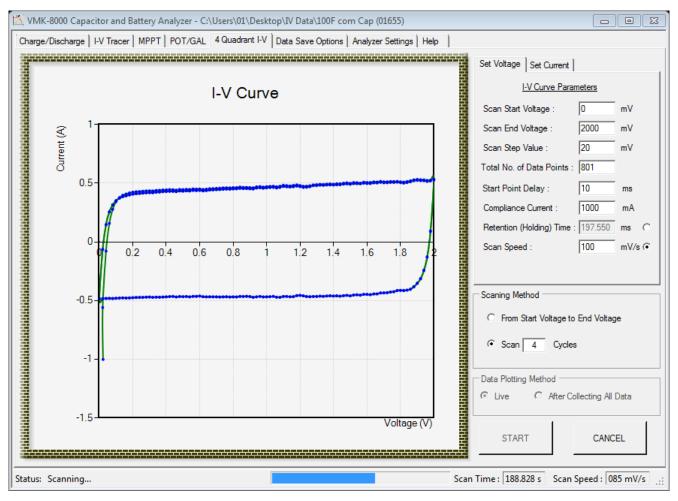
Screenshot of self discharge testing



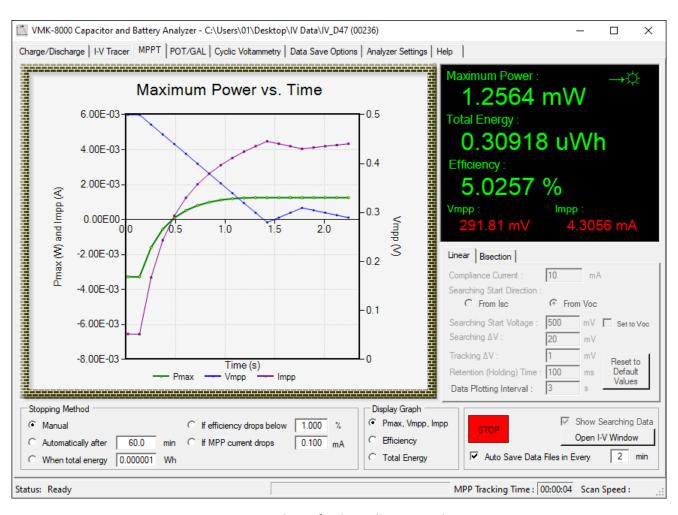
Screenshot of solar cell I-V tracing tab



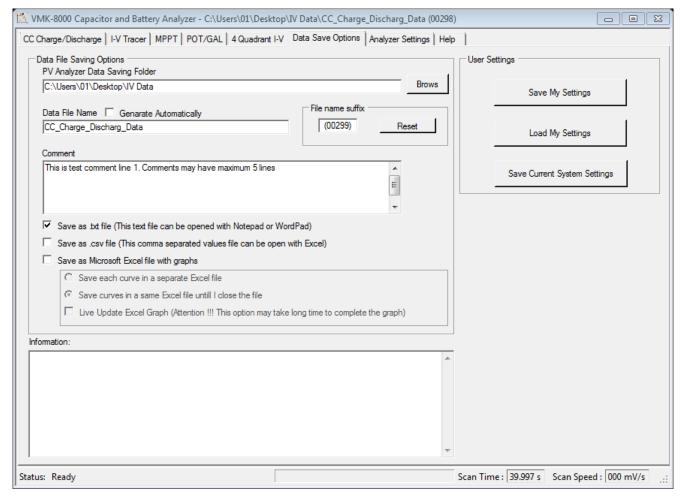
Screenshot of Galvanostat/Potentiostat tab



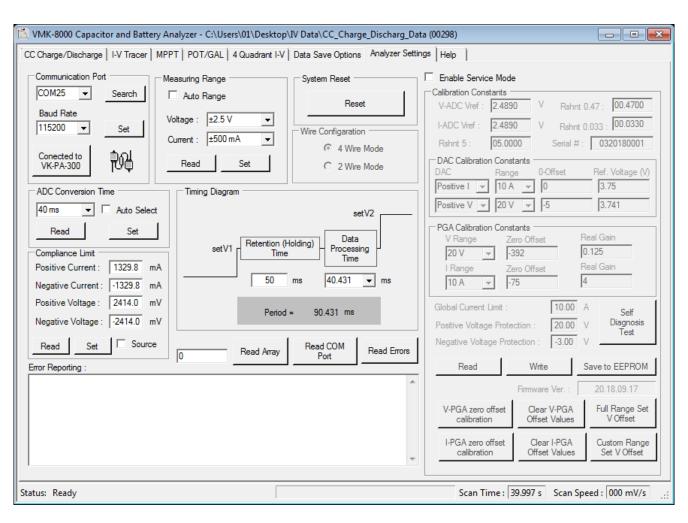
Screenshot of Cyclic Voltammetry tab



Screenshot of solar cell MPPT tab



Screenshot showing data saving options



Screenshot of "Analyzer Settings" tab

N/	ΝЛ	v o	$\boldsymbol{\cap}$	nn
V	IVI	K-8	UL	Jυ

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		V	MK-8000 Deta	ailed Electric	al Specificati	ons			
Measuring Technique			Digital Sou	Digital Source Meter with 4 probes connection to DUT.					
Measuring Range			Voltage: -	Voltage: -3 to +20 V Current: ±8 A Continuous 10 A (pulse)					
Specifications of A/D Converters Built-in Voltage Reference Parameters			Utilize on-	Resolution: 24 Bit Utilize on-chip digital calibration to eliminate offset and gain errors. ADC integration time can be selected from 16 different values from 400 ms to 33.3 μ s. Output Voltage : 2.500 \pm 0.001 V Output Voltage Drift : 3 ppm/°C (-40°C to +85°C) Output Noise : 100 nV/Hz ^½					
	Voltage measu	ıring ranges an	d reading (24-bi	t ADC) resolutio	n and voltage	setting (16-bit D	AC) resolution	S	
Range	± 250 mV	± 500 mV	± 1 V	± 2 V	± 4 V	± 8 V	± 18 V	± 20 V	
Reading Resolution	19 nV	37 nV	74 nV	149 nV	298 nV	596 nV	1.2 μV	2.4 μV	
Setting Resolution	9.5 μV	9.5 μV	19.1 μV	38.1 μV	76.3 μV	153 μV	305 μV	6010 μV	
	Current measu	ıring ranges an	d reading (24-bi	t ADC) resolution	n and current	setting (16-bit D	AC) resolutions	5	
Current Measuring Range			Curre	Current Reading Resolution (24-bit ADC)			Current Setting Resolution (16-bit DAC)		
	± 15 μA			2 pA			1 nA		
± 30 μA				5 pA			1 nA		
± 60 μA				9 pA			1 nA		
± 125 μA				18 pA			1 nA		
± 250 μA				37 pA			1 nA		
± 500 μA				74 pA			19 nA		
± 1 mA				148 pA			38 nA		
± 2 mA				295 pA			76 nA		
± 7 mA				887 pA			0.9 μΑ		
± 14 mA				1.8 nA			0.9 μΑ		
± 25 mA				3.5 nA			0.9 μΑ		
± 50 mA				7.1 nA			2 μΑ		
± 100 mA			14 nA			4 μΑ			
± 250 mA			37 nA			10 μΑ			
± 500 mA				74 nA		19 μΑ			
± 1 A				148 nA			38 μΑ		
± 2 A				282 nA			144 μΑ		
± 4 A				564 nA			289 μΑ		
± 8 A				1 μΑ			578 μΑ		
± 18 A				2 μΑ		1.1 mA			
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